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ABSTRACT OF THE DISCLOSURE

An edge correction apparatus for a digital video includes horizontal edge signal camera and vertical horizontal and vertical generators, edge signal controllers, an adder, a slice processor, and a vertical edge component suppression position detector. The horizontal and vertical edge signal generators respectively horizontal generate and vertical correction signals in the horizontal and vertical directions of a sensed image obtained via the sensing element of a digital video camera. The horizontal and vertical edge signal gain controllers control the gains of the horizontal and vertical edge correction The adder adds the horizontal and vertical edge correction signals whose gains are controlled. The slice processor adds, to the image processing signal of the digital video camera, an edge correction signal obtained by performing slice processing for an edge signal output from the adder. The vertical edge component suppression position detector causes the vertical edge signal gain controller to execute gain control of the vertical edge correction signal in accordance with а horizontal difference signal output from the horizontal edge signal generator.